Parameterized Model Order Reduction for Efficient Time and Frequency Domain Global Sensitivity Analysis of PEEC Circuits

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Abstract – This paper presents a new parameterized model order reduction technique to efficiently perform global time- and frequency-domain sensitivity analysis of electromagnetic systems over the design space of interest. The partial element equivalent circuit (PEEC) method is adopted to build the electromagnetic system model at a set of initial samples in the design space. The block Laguerre-SVD algorithm is proposed to reduce the size of the original equations of the PEEC-based equivalent circuit along with those describing the port voltage and current sensitivities. Then, a multivariate cubic spline interpolation method is used to build a parameterized compact model of port voltages and currents along with their corresponding sensitivities over the entire design space of interest. Finally, two numerical examples are presented, which confirm the accuracy and efficiency of the proposed method.

Index Terms – Parameterized model order reduction, partial element equivalent circuit, sensitivity analysis, time- and frequency-domain circuit simulation.

I. INTRODUCTION

The need to improve the performances of electromagnetic (EM) structures during their early design stage has made sensitivity analysis a necessary tool. The sensitivities represent the system response gradients in the design parameter space, where the design parameters are related to the geometry and/or the materials of the EM structure.

The simplest way to compute sensitivities is represented by the perturbation method, which requires to analyze the EM structure for two different values of each design parameter for a specific nominal point in the design space. It is computationally expensive and often inaccurate, therefore impractical when the number of design parameters to take into account is large and a global sensitivity analysis over the design space of interest is required. Recently, significant progress has been made towards the development of sensitivity analysis approaches to be used along with EM simulators, involving conducting and dielectric objects, both in time- and frequency-domain [1–8]. Differential and integral equation-based methods have been considered for sensitivity analysis [9, 10]. Typical fields of applications are optimization of microwave devices, modeling of signal integrity (SI)/power integrity (PI) problems, control of crosstalk for electromagnetic compatibility (EMC) purposes. These techniques usually turn to be highly demanding in terms of both CPU time and memory resources, since they perform the sensitivity analysis using EM solvers and/or manipulating matrices describing the EM system which are typically very large.

Among EM methods, the partial element equivalent circuit (PEEC) [11] has gained increasing popularity because of its ability to transform the EM system under examination into an equivalent circuit [11–16] that can be represented by modified nodal analysis (MNA) matrix circuit equations [17], studied by means of Kirchoff principles and simulated using circuit solvers. The PEEC method uses a circuit interpretation of the electric field integral equation (EFIE) [18].

In the context of sensitivity analysis, a PEEC-based method to carry out parameterized sensitivity analysis of EM systems that depend on multiple design parameters has been proposed in [19]. The PEEC method is used to compute state-space matrices of the MNA equations for a set of values of design parameters (e.g., geometrical and substrate parameters). An interpolation process provides parameter-
ized models of these matrices as functions of design parameters [20]. The proposed interpolation scheme is able to compute derivatives of EM matrices, which are needed to perform the system sensitivity analysis. Thus, the algorithm provides sensitivity information over the entire design space of interest (global sensitivity), and not only around one operating point (local sensitivity). Although the method [19] is very accurate and more efficient with respect to the perturbative approach, it suffers from a high computational cost when the size of the MNA matrices of the PEEC circuits becomes large. In [21], a parameterized sensitivity analysis based on a parameterized model order reduction (PMOR) technique is presented. The finite element method is used to generate the equations of the original network, a multiparameter moment matching PMOR technique and an adjoint variable method are used to calculate frequency-domain sensitivities.

In this paper, we propose a new parameterized model order reduction (PMOR) technique to efficiently perform global time- and frequency-domain sensitivity analysis of electromagnetic systems over the design space of interest. The PEEC method is adopted to generate a set of PEEC MNA equations and corresponding state-space matrices at a set of design space points. For each of these points in the design space, the block Laguerre-SVD algorithm is proposed to reduce the size of the original equations of the PEEC-based equivalent circuit along with those describing the port voltage and current sensitivities. Then, a methodology based on a multivariate cubic spline interpolation is used to build a parameterized compact model of port voltages and currents along with their corresponding sensitivities over the entire design space of interest. The proposed technique shows a significantly improved efficiency when performing a global sensitivity analysis with respect to the method [19], while maintaining a high accuracy.

The paper is organized as follows. Section II briefly describes the PEEC formulation and the sensitivity formulation while Section III presents the proposed parameterized model order reduction algorithm. Finally some numerical examples are presented in Section IV to validate the proposed technique.

II. PEEC SENSITIVITY FORMULATION

In what follows, we consider a quasi-static PEEC formulation [12]. The Galerkin’s approach is applied to convert the continuous electromagnetic problem described by the EFIE to a discrete problem in terms of electrical circuit quantities, i.e., currents $i(t)$ and node potentials $v(t)$. An example of PEEC electrical quantities for a conductor elementary cell is illustrated in the Laplace domain, in Fig. 1 where the current controlled voltage sources $s \mathbf{L}_{p,ij} I_j$ and the current controlled current sources $I_{cc}$ model the magnetic and electric field coupling, respectively. Let us denote with $n_n$ the number of the nodes and with $n_b$ the number of branches where currents flow. Among this latter, $n_c$ and $n_d$ represent the branches of conductors and dielectrics, respectively. Furthermore, let us assume to be interested in generating an admittance ($Y$) representation having $n_p$ output currents $i_p(t)$ under voltage excitation $v_p(t)$. Using the MNA formulation [17], the following admittance representation is obtained [20]:

$$
\begin{bmatrix}
P & 0 & 0 & n_n & 0 & 0 & 0 & n_n & 0 \\
0 & n_b, n_n & 0 & 0 & n_n, n_p & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & n_d, n_b & 0 & n_n, n_p & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & n_d, n_b & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & n_p, n_n & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & n_p, n_n & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & n_p, n_n \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\end{bmatrix}
\begin{bmatrix}
dq(t) \\
i(t) \\
v_d(t) \\
iv(t) \\
iv(t) \\
iv(t) \\
v_p(t) \\
iv(t) \\
iv(t) \\
\end{bmatrix}
= 
\begin{bmatrix}
0_{n_n, n_n} - PA^2 0_{n_n, n_d} PK^T \\
AP & R & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 & 0 \\
\end{bmatrix}
\begin{bmatrix}
q(t) \\
i(t) \\
v_d(t) \\
iv(t) \\
iv(t) \\
v_p(t) \\
iv(t) \\
iv(t) \\
iv(t) \\
\end{bmatrix}
+ 
\begin{bmatrix}
0_{n_n + n_d, n_p} & n_d & n_p & 0 \\
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 \\
0 & 0 & 0 & 0 \\
\end{bmatrix}
\begin{bmatrix}
q(t) \\
i(t) \\
v_d(t) \\
iv(t) \\
iv(t) \\
v_p(t) \\
iv(t) \\
iv(t) \\
iv(t) \\
\end{bmatrix},
$$

where $P \in \mathbb{R}^{n_n \times n_n}$ and $L_p \in \mathbb{R}^{n_b \times n_b}$ are the coefficients of potential and partial inductance matrices, respectively, $R \in \mathbb{R}^{n_n \times n_n}$ is a diagonal matrix containing the resistances of volume cells and $C_d \in \mathbb{R}^{n_d \times n_d}$ is the excess capacitance matrix describing the polarization charge in dielectrics [22]. $A \in \mathbb{R}^{n_n \times n_b}$ is the connectivity matrix, while $K \in \mathbb{R}^{n_p \times n_n}$ is a selection matrix introduced to define the port voltages in terms of node potentials:

$$
v_p(t) = K \cdot v(t).
$$

Fig. 1. Illustration of PEEC circuit electrical quantities for a conductor elementary cell.
In (1), \( q(t) \in \mathbb{R}^{n q \times 1} \) represents the charges on the conductors, \( i(t) \in \mathbb{R}^{n i \times 1} \) is the vector of volume currents, \( v_d(t) \in \mathbb{R}^{n v \times 1} \) describes the voltage drop across the excess capacitance and \( i_k(t) \in \mathbb{R}^{n p \times 1} \) represents the port currents. \( \mathbf{I}_{n_p \times n_p} \) is the identity matrix of dimension equal to the number of ports and \( \Phi \) is:

\[
\Phi = \begin{bmatrix} 0_{n_p \times n_d} \\ \mathbf{I}_{n_d \times n_d} \end{bmatrix}.
\] (4)

The vector \( i_p(t) \) describes the \( n_p \) port currents that are of opposite sign with respect to \( i_k(t) \). The system of equations (1)-(2) is typically ill-conditioned because the values of charges are usually much smaller than those of currents and voltages. In order to mitigate such a problem, a scaling scheme can be adopted [20]. Equations (1)-(2) can be easily translated from the \( \mathbf{Y} \) representation to an impedance \( (Z) \) representation [19] that can be expressed in a compact form as:

\[
\begin{align*}
\mathbf{C} \dot{x}(t) &= -\mathbf{G} x(t) + \mathbf{B} i_p(t) \\
\mathbf{v}_p(t) &= \mathbf{L}^T x(t),
\end{align*}
\] (5)

where \( \mathbf{C} \in \mathbb{R}^{n_c \times n_c}, \mathbf{G} \in \mathbb{R}^{n_c \times n_q}, \mathbf{B} \in \mathbb{R}^{n_c \times n_p}, \mathbf{L} = \mathbf{B}, \ x(t) = [q(t) \ i(t) \ v_d(t)]^T \in \mathbb{R}^{n \times 1} \) and \( n_q = n_n + \ n_b + n_d \) is the number of state variables [19].

Considering now the influence of the design parameters \( g = (g_1, ..., g_M) \), the formulation (5) becomes:

\[
\begin{align*}
\mathbf{C}(g) \dot{x}(t, g) &= -\mathbf{G}(g)x(t, g) + \mathbf{B}(g) i_p(t) \\
\mathbf{v}_p(t, g) &= \mathbf{L}^T(g)x(t, g).
\end{align*}
\] (6)

In [19], the sensitivity of the voltage outputs with respect to \( M \) design parameters \( g = (g_m)_{m=1}^M \) was computed deriving (6). Merging the original system and corresponding sensitivity system, a new full system can be written as:

\[
\begin{bmatrix}
\mathbf{C}(g) & 0 \\
\mathbf{C}(g) & \mathbf{C}(g)
\end{bmatrix}
\begin{bmatrix}
\dot{x}(t, g) \\
\dot{x}(t, g)
\end{bmatrix} = -
\begin{bmatrix}
\mathbf{G}(g) & 0 \\
\mathbf{G}(g) & \mathbf{G}(g)
\end{bmatrix}
\begin{bmatrix}
x(t, g) \\
\dot{x}(t, g)
\end{bmatrix} +
\begin{bmatrix}
\mathbf{B}(g) & 0 \\
\mathbf{B}(g) & \mathbf{B}(g)
\end{bmatrix}
\begin{bmatrix}
i_p(t, g) \\
\dot{i}_p(t, g)
\end{bmatrix},
\] (7)

where \( \hat{\cdot} \) denotes the derivatives with respect to the design parameters. Equation (7) can be solved, applying appropriate termination conditions, by the means of differential equations solvers, upon the knowledge of the system matrices and their derivatives. However, the simulations become slow and difficult to manage when the dimensions of the original PEEC matrices become large. Therefore, it is fundamental to obtain a parameterized reduced order model able to reduce the CPU time effort needed to carry out the desired simulations.

III. PARAMETERIZED MODEL ORDER REDUCTION ALGORITHM

In this section, we describe the proposed PMOR algorithm applied to the system (7) in order to perform parameterized (global) time- and frequency-domain sensitivity analysis with respect to design parameters in a more efficient way in comparison with the technique [19], where a parameterized sensitivity analysis was performed using interpolation models of the original PEEC matrices without any order reduction scheme.

A. Block model order reduction

The first step of the proposed PMOR algorithm is to generate a set of PEEC matrices \( \{\mathbf{P}(g_k), \ \mathbf{L}_p(g_k), \mathbf{C}(g_k), \mathbf{R}(g_k)\}_{k=1}^{K_{tot}} \) corresponding to a set of \( K_{tot} \) initial samples \( g_k \) in the design space. We assume that a topologically fixed discretization mesh is used and that it is independent from the specific design parameter values as in [19]. When geometrical parameters are modified, the mesh is only locally stretched or shrunk. Therefore, the PEEC matrices \( \mathbf{A}, \mathbf{F}, \mathbf{K} \) are uniquely determined by the circuit topology and do not depend on \( g \). Then, a model order reduction (MOR) is proposed to generate the Krylov matrix \( \mathbf{K}_q(g_k) \) of the system (7) for each initial sample in the design space. In [23, 24], block structure preserving MOR methods were presented, where blocks were derived based on the specific application. This concept of block structure preserving MOR is used in this paper to generalize the Laguerre-SVD MOR (LSVD-MOR) algorithm [25]. The standard LSVD-MOR is listed in Algorithm 1, where \( \alpha \) is a positive scaling parameter, \( q - 1 \) is the order of approximation and:

\[
\mathbf{K}_q = \begin{bmatrix} \mathbf{R}^{(0)} & \mathbf{R}^{(1)} & \ldots & \mathbf{R}^{(q-1)} \end{bmatrix},
\] (8)

is the Krylov matrix of order \( q - 1 \) [25]. The LSVD-MOR algorithm can be extended to a block LSVD-MOR method, considering the block form of the matrices in (7). If we replace the set of matrices \( \{\mathbf{C}, \mathbf{G}, \mathbf{B}, \mathbf{L}\} \) in Algorithm 1 with that block form, the step \( k = 0 \) of the standard LSVD-MOR algorithm can be re-written as:

\[
\begin{bmatrix}
\mathbf{G} & + \alpha \mathbf{C} \\
\mathbf{G} & + \alpha \mathbf{C} + \alpha \mathbf{C}
\end{bmatrix}
\begin{bmatrix}
\mathbf{R}^{(0)} & \mathbf{R}^{(1)} \\
\mathbf{R}^{(0)} & \mathbf{R}^{(1)}
\end{bmatrix} = 
\begin{bmatrix}
\mathbf{B} \\
\mathbf{B}
\end{bmatrix}.
\] (9)

After some manipulation it follows:

\[
\begin{bmatrix}
\mathbf{R}^{(0)} & \mathbf{R}^{(0)} \\
\mathbf{R}^{(1)} & \mathbf{R}^{(1)}
\end{bmatrix}
\begin{bmatrix}
\mathbf{G}_N & \mathbf{G}_N^{-1} \mathbf{B} \\
\mathbf{G}_N & \mathbf{G}_N^{-1} \mathbf{B}
\end{bmatrix}
\begin{bmatrix}
\mathbf{R}^{(0)} & \mathbf{R}^{(0)} \\
\mathbf{R}^{(1)} & \mathbf{R}^{(1)}
\end{bmatrix} = 
\begin{bmatrix}
\mathbf{R}^{(0)} & \mathbf{R}^{(0)} \\
\mathbf{R}^{(1)} & \mathbf{R}^{(1)}
\end{bmatrix}.
\] (10)
where we have defined $G_N$ and $\tilde{G}_N$ as:
\[
G_N = G + \alpha C, \\
\tilde{G}_N = G + \alpha \tilde{C}.
\] (11)

At the step $k = 0$, the Krylov matrix $K_q$ is composed of two components:
\[
\begin{bmatrix}
R_1^{(0)} = G_N^{-1}B \\
R_2^{(0)} = \tilde{G}_N^{-1}B + G_N^{-1}\tilde{B}
\end{bmatrix}
\] (12)

The first component is related to the Krylov matrix of the original system (5) for $k = 0$, while the second one is the derivative of this first component:
\[
R_2^{(0)} = \tilde{R}_1^{(0)}. 
\] (13)

This means that the Krylov matrix of the original system and its derivative are enough to finalize the first step of the block LSVD-MOR algorithm. The other steps for for $k > 0$ lead to:
\[
\begin{bmatrix}
G + \alpha C & 0 \\
G + \alpha \tilde{C}G + \alpha \tilde{C}
\end{bmatrix}
\begin{bmatrix}
R_1^{(k)} \\
R_2^{(k)}
\end{bmatrix} = 
\begin{bmatrix}
G - \alpha C & 0 \\
G - \alpha \tilde{C}G - \alpha \tilde{C}
\end{bmatrix}
\begin{bmatrix}
R_1^{(k-1)} \\
R_2^{(k-1)}
\end{bmatrix}
\] (14)

Likewise, after some manipulations:
\[
\begin{bmatrix}
R_1^{(k)} & R_2^{(k)} \\
R_21 & R_2^{(k)}
\end{bmatrix} = 
\begin{bmatrix}
G^{-1} & 0 \\
G^{-1} & G^{-1}
\end{bmatrix}
\begin{bmatrix}
R_1^{(k-1)} \\
R_2^{(k-1)}
\end{bmatrix}
\] (15)

where we have defined $G_R$ and $\tilde{G}_R$ as:
\[
G_R = G - \alpha C, \\
\tilde{G}_R = G - \alpha \tilde{C}.
\] (16)

The blocks of the Krylov matrix for $k > 0$ are also composed of two components:
\[
\begin{align*}
R_1^{(k)} &= G_N^{-1}G_R R_1^{(k-1)} \\
R_2^{(k)} &= G_N^{-1}G_R R_2^{(k-1)} + \\
& \quad \left(G_N^{-1}G_R + G_N^{-1}\tilde{G}_R\right) R_1^{(k-1)}
\end{align*}
\] (17)

As previously, equation (17) shows that the first component $R_1^{(k)}$ is the component of the Krylov matrix of the original system (5) for $k > 0$, while the second component $R_2^{(k)}$ is the corresponding derivative:
\[
R_2^{(k)} = \tilde{R}_1^{(k)}.
\] (18)

According to Algorithm 1, the Krylov matrix $K_q$ reads:
\[
K_q = 
\begin{bmatrix}
R_1^{(0)} & 0 & R_1^{(1)} & \ldots & R_1^{(q-1)} & 0 \\
R_2^{(0)} & R_1^{(0)} & R_2^{(1)} & \ldots & R_2^{(q-1)} & \tilde{R}_1^{(q-1)}
\end{bmatrix},
\] (19)

and by a column permutation:
\[
K_q = 
\begin{bmatrix}
R_1^{(0)} & \ldots & R_1^{(q-1)} & 0 & \ldots & 0 \\
R_2^{(0)} & \ldots & R_2^{(q-1)} & \tilde{R}_1^{(q-1)} & \ldots & \tilde{R}_1^{(q-1)}
\end{bmatrix},
\] (20)

where $R_1^{(i)} = R^{(i)}$ is the $i$-th component of the Krylov matrix of the original system and $R_2^{(i)} = \tilde{R}^{(i)}$ is its derivative (see (13) and (18)). For each initial sample $g_k$ the corresponding Krylov matrix $K_q(g_k)$ is computed using the block LSVD-MOR method.

To compute the reduced system of (7), an orthonormalization step is applied to the Krylov matrix $K_q$ using a Singular Value Decomposition (SVD) algorithm to obtain the projection matrix:
\[
V \rightarrow V \Sigma U^T = \text{SVD}(K_q),
\] (21)

that is then used for the congruence transformations to compute the reduced matrices. In order to preserve the block structure of the system (7), the projection matrix $V \in \mathbb{R}^{2n_r \times n_r}$ is partitioned in a block fashion [23, 24]:
\[
V = \begin{bmatrix}
V_1 & 0 \\
0 & V_2
\end{bmatrix},
\] (22)

where $n_r = q \cdot n_p$ denotes the column size of the Krylov matrix $K_q$. The projection matrices $V_1$ and $V_2$ may differ with respect to the column size of $V_1$ and $V_2$. However, this does not influence the reduction of the full system (7). Finally the projection matrix is written as [23, 24]:
\[
\tilde{V} = \begin{bmatrix}
\tilde{V}_1 & 0 \\
0 & \tilde{V}_2
\end{bmatrix},
\] (23)

In order to reduce the overall system in (7), the projection matrix (23) is used to compute the reduced matrices by congruence transformations:
\[
\begin{bmatrix}
C_{r,1} & 0 \\
\tilde{C}_{r,1} & C_{r,2}
\end{bmatrix} = \begin{bmatrix}
\tilde{V}_1^T & 0 \\
0 & \tilde{V}_2^T
\end{bmatrix} \begin{bmatrix}
C_0 & \tilde{V}_1 \\
\tilde{C}C & \tilde{V}_2
\end{bmatrix}
\] (24)

Algorithm 2 lists the steps of the proposed block LSVD-MOR method to perform in order to obtain
the set of reduced matrices for the reduced system. Concerning the proposed PMOR algorithm, the block LSVD-MOR method is used to generate the Krylov matrix $K_i(g_k)$ of the system (7) for each initial sample in the design space.

**B. Interpolation models**

Once the set of matrices $\{P(g_k), L_p(g_k), C_d(g_k), R(g_k), K_q(g_k)\}_{k=1}^{K_{int}}$ is available, the corresponding interpolation models are built. Figure 2 shows an example for the case of one design parameter. Each interpolation model is built starting from a set of samples indicated with red dots (○) in Fig. 2. For each (○) sample in the design space, a set of PEEC matrices and a Krylov matrix are computed and the corresponding models, that cover the entire design space, are built by the means of an interpolation scheme. For interpolation purposes the multivariate cubic spline interpolation method [26], which is well-known for its stable and smooth characteristics, is used. The proposed interpolation scheme is continuous in the first and second order derivatives and can be used in the general case of an M-dimensional (M-D) design space.

First, the interpolation models $\{P(g), L_p(g), C_d(g), R(g)\}$ are computed while guaranteeing positive definiteness and semidefiniteness matrix properties [19, 20]. At this step, according to Algorithm 2, it could be possible to compute a set of reduced matrices for each point of interest in the design space by using the block LSVD-MOR method previously described. However, to improve the efficiency of the proposed PMOR method, we choose to create an interpolation model for the $K_q(g)$ matrix, starting from the corresponding data samples $K_i(g_k)$. From the model $\overline{K_q}(g)$, the values of the projection matrices $V_1(g)$ and $V_2(g)$ can be computed for each point of interest in the design space.

Inspecting equation (19), only the interpolation models of the matrices $\{R_1^{(i)}(g_k)\}_{i=0}^{q-1}$ and $\{R_2^{(i)}(g_k)\}_{i=0}^{q-1}$ (namely $\overline{R}_1^{(i)}(g)_{i=0}^{q-1}$ and $\overline{R}_2^{(i)}(g)_{i=0}^{q-1}$) are sufficient to obtain the interpolation model $\overline{K}_q(g)$. Furthermore, there is still one degree of freedom about how to compute the matrices $\{R_2^{(i)}(g_k)\}_{i=0}^{q-1}$:

1. As described in Algorithm 2;
2. directly as derivative of $\overline{R}_1^{(i)}(g)_{i=0}^{q-1}$ (see (13) and (18)).

The latter option is based on the interpolation model $\overline{R}_1^{(i)}(g)_{i=0}^{q-1}$ that can provide derivatives with respect to $g$. This choice is preferred since it allows storing only the model $\overline{R}_1^{(i)}(g)_{i=0}^{q-1}$ and then saving memory resources, while keeping a similar accuracy.

Using the interpolation models $\{P(g), L_p(g), C_d(g), R(g)\}$ and $\overline{K}_q(g)$, it is possible to obtain a set of PEEC matrices and a set of projection matrices $V_1(g)$ and $V_2(g)$ for each point of the design space. Applying the congruence transformations, the sets of reduced matrices $\{C_{r,1}(g), G_{r,1}(g), B_{r,1}(g), L_{r,1}(g)\}_{i=1,2}$ and $\{C_{r,2}(g), G_{r,2}(g), B_{r,2}(g), L_{r,2}(g)\}$ are computed. The reduced version of (7) can be written as:

$$\begin{pmatrix} C_{r,1}(g) & 0 \\ C_{r,2}(g) & C_{r,2}(g) \end{pmatrix} \begin{pmatrix} \hat{x}_r(t, g) \\ \tilde{x}_r(t, g) \end{pmatrix} = \begin{pmatrix} G_{r,1}(g) & 0 \\ G_{r,2}(g) & G_{r,2}(g) \end{pmatrix} \begin{pmatrix} x_r(t, g) \\ \tilde{x}_r(t, g) \end{pmatrix} + \begin{pmatrix} B_{r,1}(g) & 0 \\ B_{r,2}(g) & B_{r,2}(g) \end{pmatrix} \begin{pmatrix} \hat{i}_p(t, g) \\ \tilde{i}_p(t, g) \end{pmatrix}$$

$$\begin{pmatrix} v_p(t, g) \\ \tilde{v}_p(t, g) \end{pmatrix} = \begin{pmatrix} L_{r,1}(g) & L_{r,2}(g) \end{pmatrix}^T \begin{pmatrix} x_r(t, g) \\ \tilde{x}_r(t, g) \end{pmatrix}.$$  

These reduced matrices are used to perform both time- and frequency-domain sensitivity analysis, with appropriate termination conditions.

**IV. NUMERICAL EXAMPLES**

Two numerical examples are proposed to validate the proposed PMOR technique for sensitivity analysis. Parameterized time- and frequency-domain sensitivity analyses are performed with the proposed PMOR technique and the results are compared with the approach proposed in [19] and with the perturbative approach (with respect to each parameter $g_m$) that in time- and frequency-domain reads:

$$\hat{v}_{p,g_m}(t, g) = \frac{v_p(t, g_1 \ldots g_m + \Delta g_m \ldots g_M) - v_p(t, g)}{\Delta g_m},$$

$$\hat{Z}_{g_m}(s, g) = \frac{Z(s, g_1 \ldots g_M + \Delta g_m \ldots g_M) - Z(s, g)}{\Delta g_m}.$$
where $m = 1, \ldots, M$, $M$ and $\Delta g_m$ represent the number of parameters and the increment, respectively. The accuracy of the perturbative approach depends on the choice of the increment $\Delta g_m$: if the increment is not small enough, the estimation of the derivative is not accurate, while if the perturbation is very small compared with the nominal value, numerical problems may occur due to numerical noise. This may lead to inaccurate computation of the system sensitivities. In contrast, thanks to the interpolation models, the methods presented in this paper and in [19] lead to more accuracy and numerical stability, since the derivatives are computed from continuously differentiable polynomials built by means of spline functions. The method [19] is denoted as Full Parameterized while the proposed method is denoted as Block PMOR in what follows.

In the numerical results (see Tables 1-2), for the Full Parameterized and Block PMOR models, the model evaluation CPU time indicates the average time needed to evaluate the corresponding parameterized models in a point of the validation grid in order to obtain a set of PEEC matrices. Moreover, for the Block PMOR model, the SVD operation is also part of the model evaluation. For the Perturbative Approach, the model evaluation CPU time refers to the average time needed to compute a set of PEEC matrices by a PEEC solver at and around a point in the validation grid, which are then used for a finite difference calculation. Once the parameterized models are evaluated, or PEEC matrices have been computed (perturbative approach), they can be used to carry out sensitivity analysis in frequency- and time-domain. For each of the three methods, the average time needed to perform the sensitivity analysis in a point of the validation grid is denoted as simulation CPU time.

Numerical simulations have been performed on a Linux platform on an AMD FX(tm)-6100 Six-Core Processor 3.3 GHz with 16 GB RAM.

A. Metallic enclosure coupled to a transmission line

In the first example a metallic enclosure coupled to a transmission line is studied. The cross section is shown in Fig. 3. The geometrical dimensions are $w_c = 1\text{ mm}$, $d_c = 5\text{ mm}$, $s_1 = 1\text{ mm}$, $d_1 = 30\text{ mm}$. A set of PEEC matrices is computed over a grid of $6 \times 6$ values of the conductor length $l_c \in [25 - 65] \text{ mm}$ and $s_c \in [7 - 22] \text{ mm}$ in order to build the aforementioned parameterized reduced order model. Furthermore, a parameterized full model has been built by means of the method [19] for the comparison. The order of the full models is 2870 while the reduced order is 471. Parameterized time- and frequency-domain sensitivities are performed over a validation grid of $5 \times 5$ values of $l_c \in [29 - 61] \text{ mm}$ and $s_c \in [8.5 - 20.5] \text{ mm}$. The obtained results are then compared with the ones obtained by the perturbative model (26), (27). For the time-domain results, the bottom conductor is excited by a smooth pulse voltage source with amplitude 1V, rise/fall times $\tau_r = \tau_f = 1.5 \text{ ns}$, width 6 ns and internal resistance $R_T = 50 \text{ } \Omega$. All the ports are terminated on 50 Ω resistances.

Figures 4-5 and 6-7 show time- and frequency-domain results that confirm the high accuracy of the proposed approach. Table 1 clearly shows the computational advantage of the proposed PMOR technique: the CPU time required to perform both time- and frequency-domain sensitivity simulations is considerably improved with respect to the two other compared approaches for a sensitivity analysis around a nominal design point and therefore also for a global sensitivity analysis. It is important to note that the Block PMOR and Full Parameterized methods require a one-time effort to build the corresponding parameterized models that then allow global sensitivity simulations. This CPU time for the Block PMOR method is equal to 8236 s and for the Full Parameterized method is equal to 7513 s.

![Fig. 3. Metallic enclosure coupled to a transmission line (example A).](image-url)

Table 1: Simulation time comparison (Example A). The CPU time information related to time- and frequency-domain analysis refers to an average value of the CPU time needed to perform the sensitivity analysis in a point of the validation grid.

<table>
<thead>
<tr>
<th>Method</th>
<th>Block PMOR</th>
<th>Full Parameterized</th>
<th>Perturbative Approach</th>
</tr>
</thead>
<tbody>
<tr>
<td>Time</td>
<td>Simulation</td>
<td>6 s</td>
<td>12 s</td>
</tr>
<tr>
<td>Frequency</td>
<td>Simulation</td>
<td>37 s</td>
<td>639 s</td>
</tr>
<tr>
<td>Total</td>
<td>Simulation</td>
<td>53 s</td>
<td>651 s</td>
</tr>
</tbody>
</table>
Fig. 4. Time-domain voltage sensitivity with respect to $l_c$ at the port 1 (example A).

Fig. 5. Time-domain voltage sensitivity with respect to $s_c$ at the port 2 (example A).

Fig. 6. Magnitude of the sensitivity of $Z_{11}$ with respect to $s_c$ (example A).

Fig. 7. Magnitude of the sensitivity of $Z_{12}$ with respect to $l_c$ (example A).

Fig. 8. Three conductors microstrip (example B).

B. Three conductors microstrip

Three coupled microstrips are modeled in this example and Fig. 8 shows the corresponding cross section. The geometrical dimensions are $w_c = 178 \mu m$, $t_c = 35 \mu m$, $d = 3 mm$ and the length of the lines is $l = 40 mm$. The parameterized reduced order and parameterized full models have been built starting from a set of PEEC matrices computed over a grid of $6 \times 6$ values of $s_c \in [0.1 - 0.4] mm$ and $h \in [0.1 - 0.3] mm$. The order of the full models is 3360 while the reduced order is 577. Parameterized time- and frequency-domain sensitivities are performed over a validation grid of $5 \times 5$ values of $s_c \in [0.13 - 0.37] mm$ and $h \in [0.12 - 0.28] mm$. The obtained results are then compared with the perturbative approach (26), (27). For the time-domain results, a smooth pulse voltage source with amplitude $1V$, rise/fall times $\tau_r = \tau_f = 1.5 ns$, width 6 ns and internal resistance $R_T = 50 \Omega$ is applied on the first conductor. All the ports are terminated on 50 $\Omega$ resistances. Time-domain sensitivity results are shown in Figs. 9, 10 while frequency-domain sensitivity results are shown in Figs. 11, 12. As in the previous example, the results confirm the high accuracy of the proposed method. Table 2 shows the simulation time comparison that clearly confirms that the proposed PMOR method considerably reduces the CPU time.
required for a sensitivity analysis with respect to the other two methods around a nominal design point and therefore for a global sensitivity analysis. As in the previous example, an initial computational effort is required to compute the Block PMOR and Full Parameterized models that is equal to 9306 s for the Block PMOR and to 8456 s for the Full Parameterized method.

V. CONCLUSIONS

In this paper we have presented a new PMOR technique to perform both time- and frequency-domain global sensitivity analysis of PEEC circuits. It is based on the PEEC method, the block Laguerre SVD model order reduction technique and interpolation schemes. Two numerical examples confirm the high modeling capability and the improved efficiency of the proposed approach with respect to existing sensitivity analysis methods.
VI. APPENDIX

This appendix illustrates the pseudo-code of the Laguerre-SVD and the Block Laguerre-SVD algorithms.

Algorithm 1 Laguerre-SVD Algorithm

<table>
<thead>
<tr>
<th>Select values for $\alpha$ and $q$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$R^{(0)} \leftarrow (G + \alpha C) R^{(0)} = B$</td>
</tr>
<tr>
<td>for $k = 1 \rightarrow q - 1$ do</td>
</tr>
<tr>
<td>$R^{(k)} \leftarrow (G + \alpha C) R^{(k)} = (G - \alpha C) R^{(k-1)}$</td>
</tr>
<tr>
<td>end for</td>
</tr>
<tr>
<td>$K_q = [R^{(0)}, R^{(1)}, \ldots, R^{(q-1)}]$</td>
</tr>
<tr>
<td>$V \Sigma U^T = \text{SVD} (K_q)$</td>
</tr>
<tr>
<td>$C_r = V^T C V$, $G_r = V^T G V$</td>
</tr>
<tr>
<td>$B_r = V^T B_r$, $L_r = V^T L_r$</td>
</tr>
</tbody>
</table>

Algorithm 2 Block Laguerre-SVD Algorithm

<table>
<thead>
<tr>
<th>Select values for $\alpha$ and $q$</th>
</tr>
</thead>
<tbody>
<tr>
<td>$G_N \leftarrow G + \alpha C$, $G_R \leftarrow G - \alpha C$</td>
</tr>
<tr>
<td>$G_N \leftarrow G + \alpha C$, $G_R \leftarrow G - \alpha C$</td>
</tr>
<tr>
<td>$R^{(0)}_1 \leftarrow G_N^{-1} B$</td>
</tr>
<tr>
<td>$R^{(0)}_2 \leftarrow G_N^{-1} B + G_N^{-1}$</td>
</tr>
<tr>
<td>for $k = 1 \rightarrow q - 1$ do</td>
</tr>
<tr>
<td>$R^{(k)}_1 \leftarrow G_N^{-1} G_R R^{(k-1)}_1$</td>
</tr>
<tr>
<td>$R^{(k)}_2 \leftarrow \left( G_N^{-1} G_R + G_N^{-1} \hat{G}_R \right) R^{(k-1)}_2 + G_N^{-1} G_R R^{(k-1)}_2$</td>
</tr>
<tr>
<td>end for</td>
</tr>
<tr>
<td>$K_q = \left[ \begin{array}{c} \hat{R}^{(0)}_1 \cdots \hat{R}^{(q-1)}_1 \ 0 \cdots 0 \ \hat{R}^{(q-1)}_2 \cdots \hat{R}^{(q-1)}_2 \ \hat{R}^{(0)}_2 \cdots \hat{R}^{(0)}_2 \end{array} \right]$</td>
</tr>
<tr>
<td>$n_r = q^r n_p$</td>
</tr>
<tr>
<td>$V \leftarrow V \Sigma U^T = \text{SVD} (K_q)$</td>
</tr>
<tr>
<td>$V = \left[ \begin{array}{c} V_1 \ V_2 \end{array} \right]$</td>
</tr>
<tr>
<td>for $i = 1, 2$ do</td>
</tr>
<tr>
<td>If rank $V_i = r_{V_i} &lt; n_r$ determine an $n_s \times r_{V_i}$ matrix $\hat{V}_i$ with $\text{colspan} ; \hat{V}<em>i = \text{colspan} ; V_i$ and rank $V_i = r</em>{V_i}$</td>
</tr>
<tr>
<td>end for</td>
</tr>
<tr>
<td>$\hat{V} = \left[ \begin{array}{c} \hat{V}_1 \ 0 \ 0 \end{array} \right]$</td>
</tr>
<tr>
<td>Apply congruence transformations (24)</td>
</tr>
</tbody>
</table>

ACKNOWLEDGMENT

This work was supported by the Research Foundation Flanders (FWO-Vlaanderen). Ferranti is currently a Post-Doctoral Research Fellow of FWO-Vlaanderen.

REFERENCES


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